

FIG. 1 is a block diagram of a computer system 100. The system includes a processor 110, a host bus 120, a memory controller hub (MCH) 130, a system memory 140, a peripheral bridge 190, a DT buffer circuit 192, a primary bus 195, a secondary peripheral bus 185, an input/output controller hub (ICH) 150, a mass storage device 170, a CD ROM 172, a floppy 174, a hard drive 176, and various I/O devices and PCI devices connected via buses 160, 165, 180, and 187.

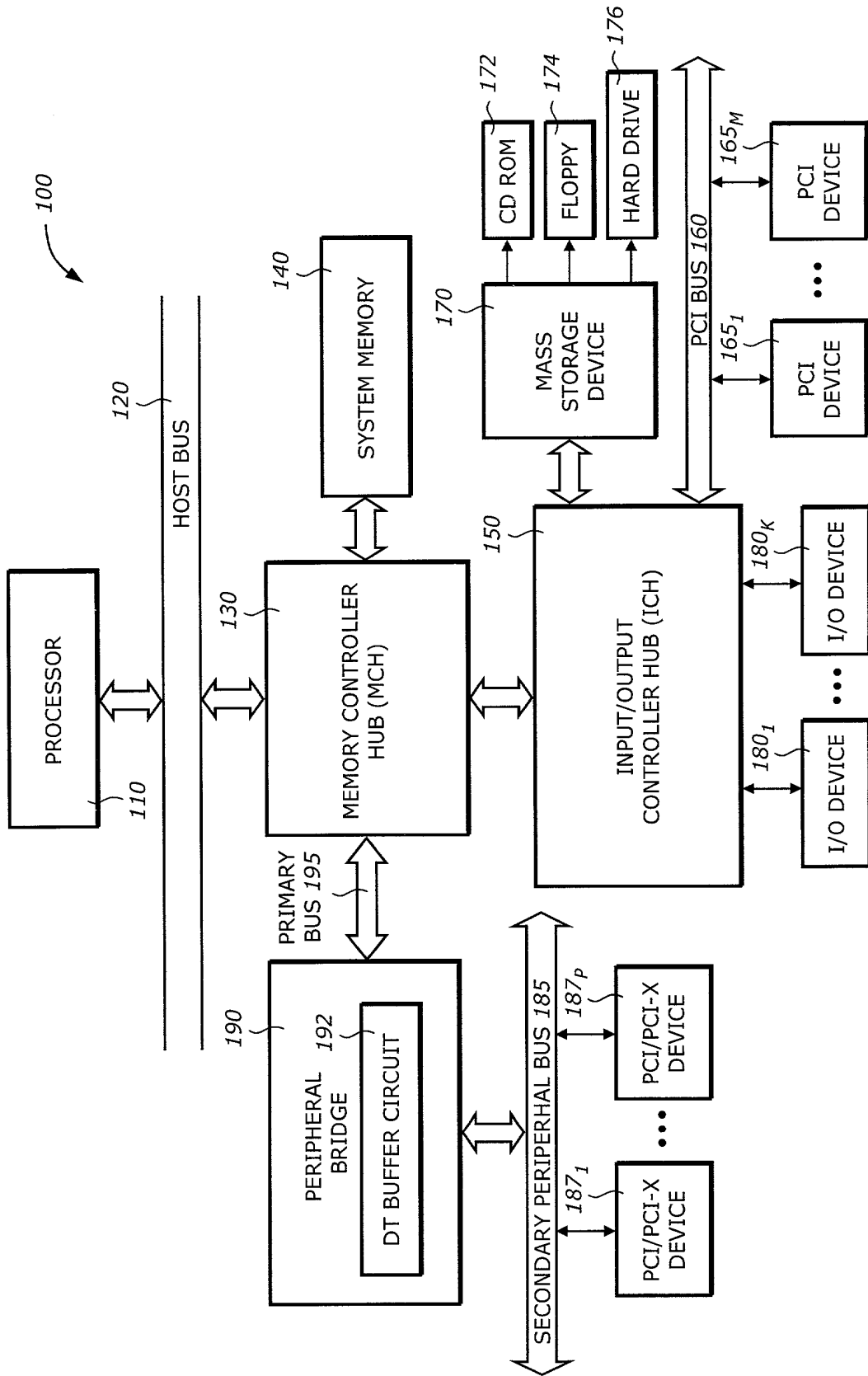


FIG. 1

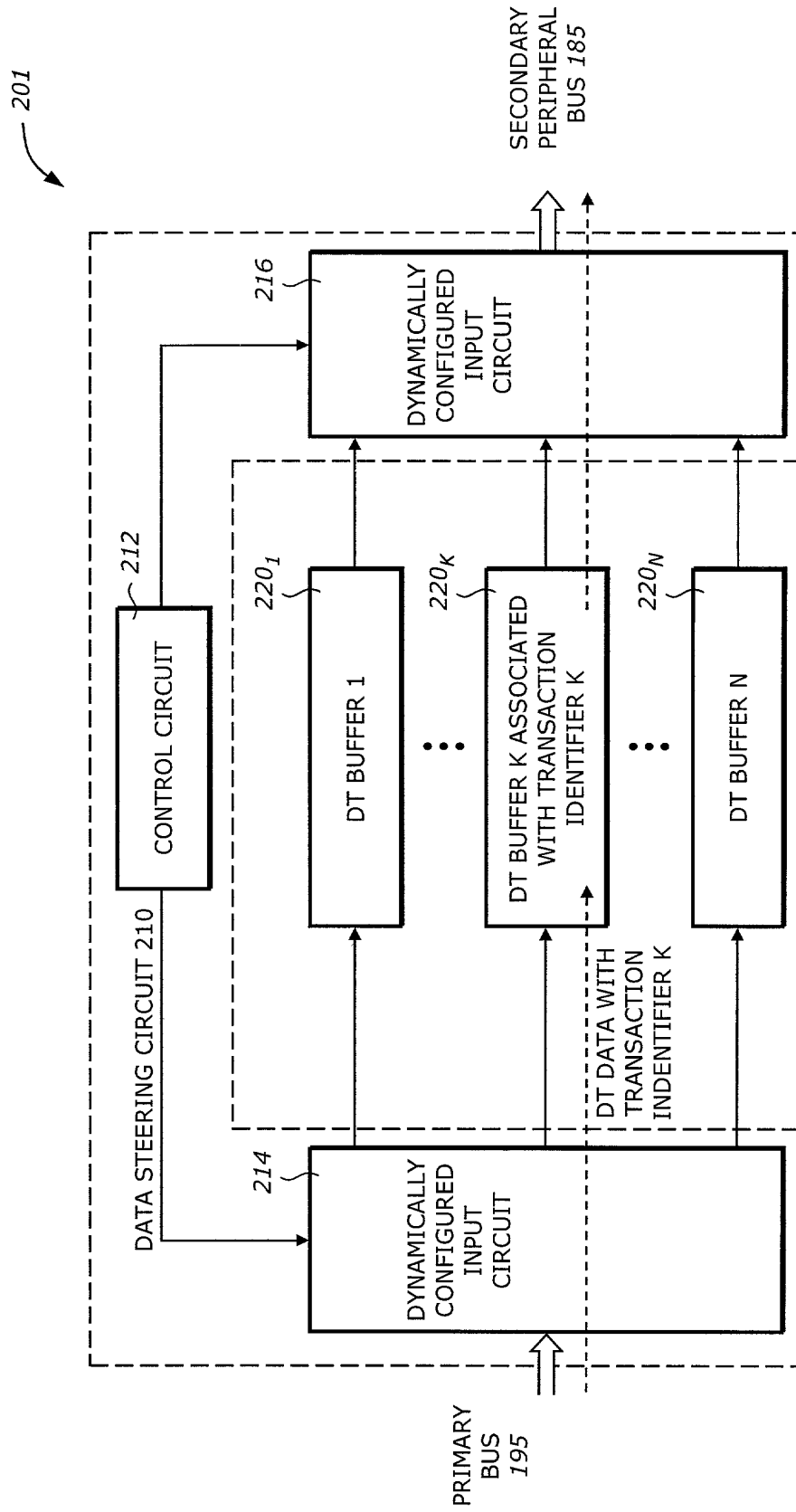


FIG. 2

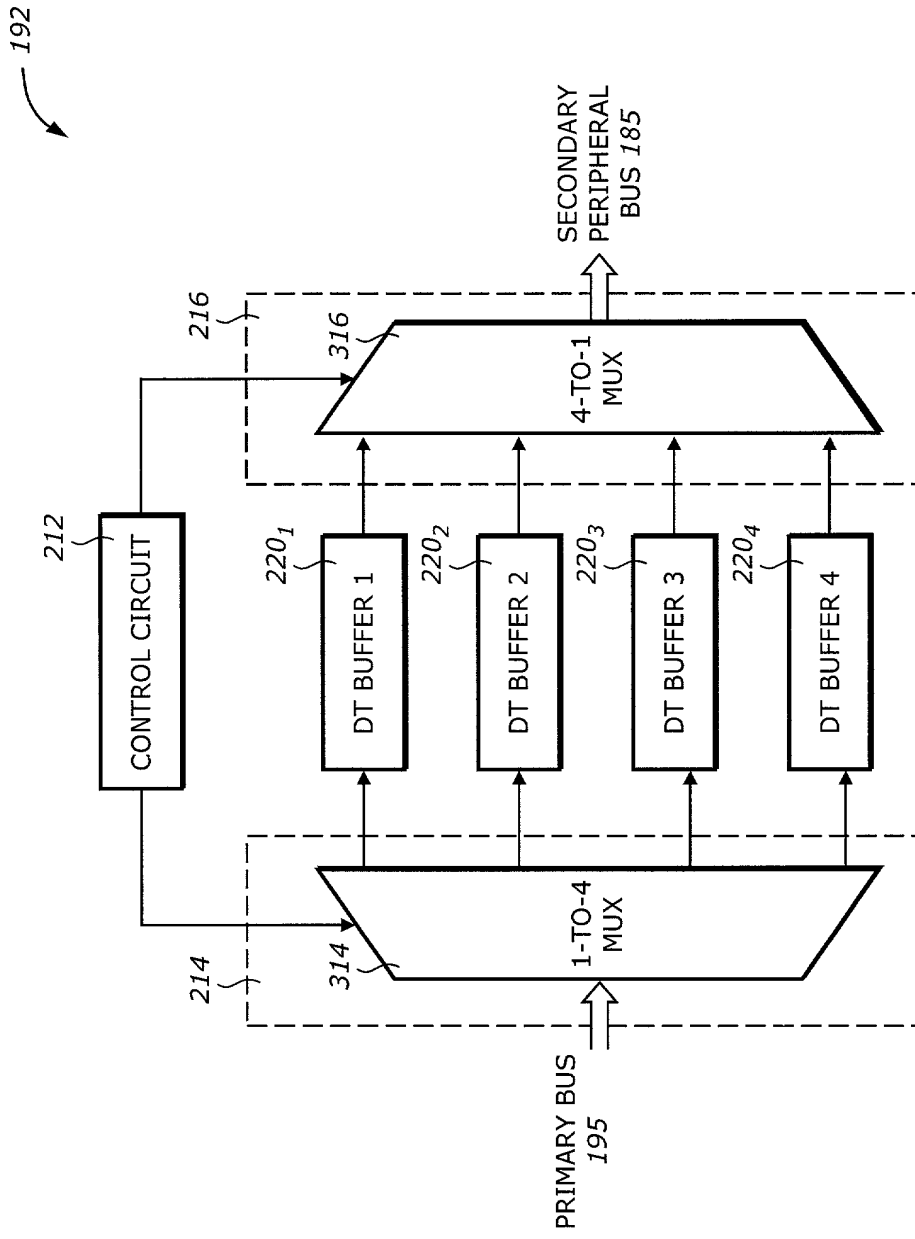


FIG. 3A

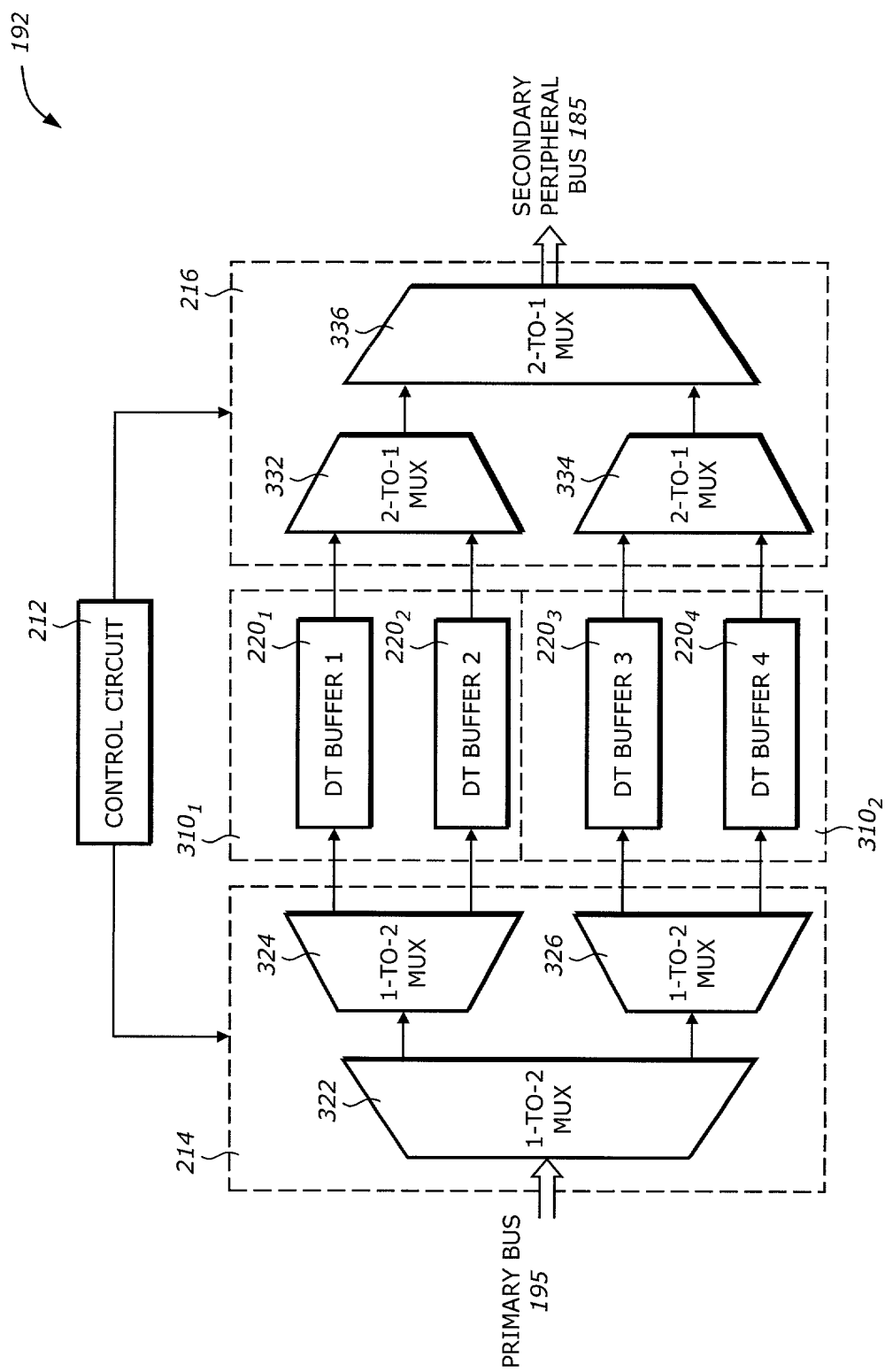


FIG. 3B

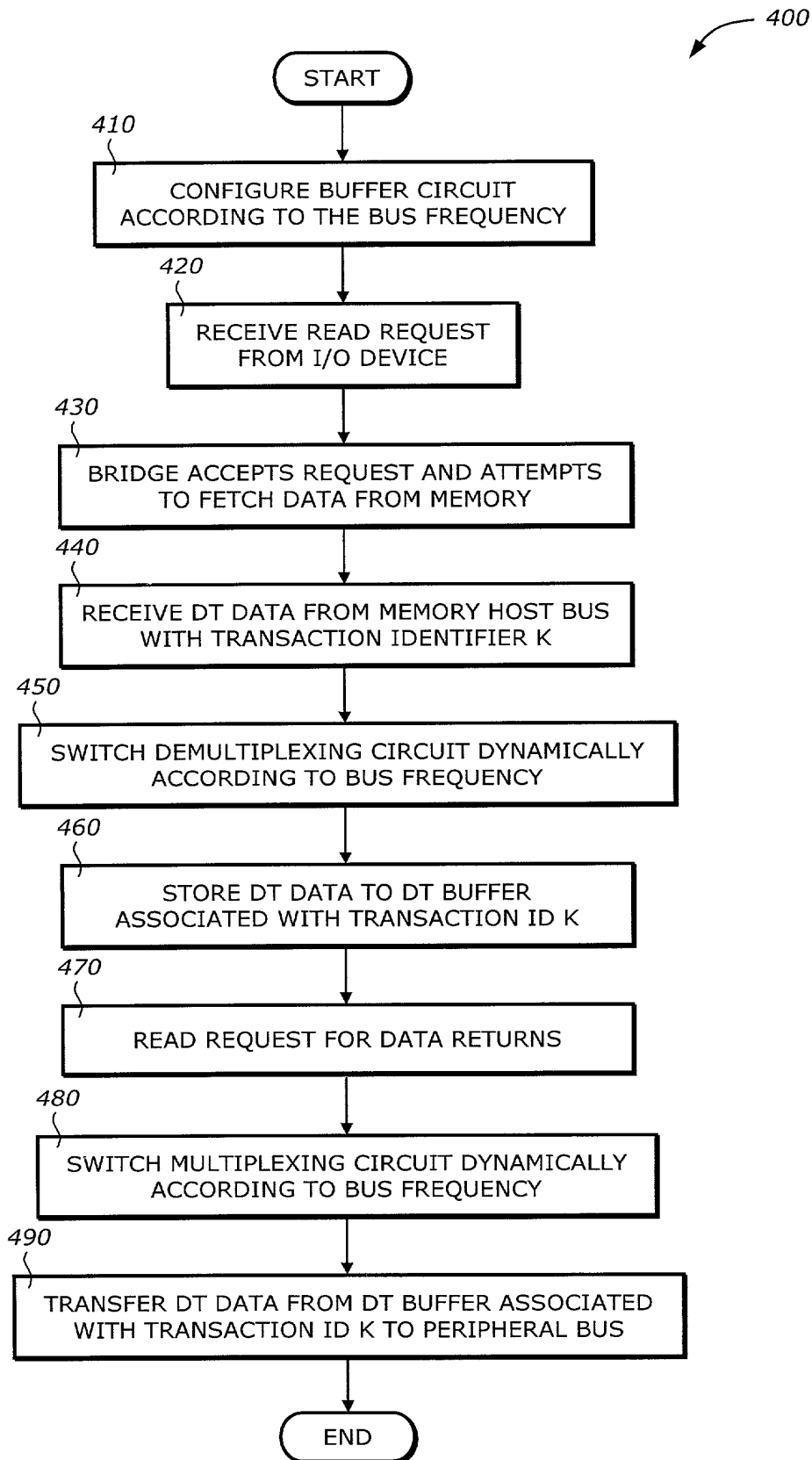


FIG. 4